less power drawn (less power (less power)

2.4.14 Procedure Consensus
- overview procedure consensus
- in one processor chip

Multiple CPUs
2. Run one app. Perform these:
   - Run multiple apps simultaneously.
   - Run app every second, every minute:
     - Show import.
     - Show import.
Access

NDRM: No Remote Mem

No, No: Non-UMA

No: UMA: Custom Memory

Short Memory

MIMD Processor
The memory speed decreases as CPU speeds are much higher.
NOT OK

load x

while \( x \neq 0 \)

loop x

store x

The problem coherence cache

STO x

++ x

loop x

\( x = 0 \)
Ca. l. a. single copy semantic
consistent usage
returns the most
coherence = every read
Conventional caches
multithreaded (CPU) must here
\[ x = 5 \quad \text{or} \quad x = 0 \]
$T_{20}$

Proof

1. \[ x \]
2. \[ \text{set} \quad x \]
3. \[ \text{let} \quad y \]
4. \[ 1 + x = x \]
5. \[ x = x + 1 \]
6. \[ 0 = x \]
battle
neck
Cases - larger mean height
and prevalence - more

Statements
- School-based at una
probably 8, these days.

8 CPU = max?

4 CPU = max?
Centrality

data centers, clusters, grid

= 1.5

ORMA

1. Software buzz clusters
2. Hardware buzz clusters
Cluster

Machines running Operating System (Linux)

MPI

1 application

Runs 4 processes
She made many mistakes that prevented the USA