Privileged mode → User protection

- Memory protection

- Process/Thread registers
Software

- 'infrequent' or 'generous by

'extensive' or 'generous' by

'nineteenth'
4 Stacks w/ RAM

Level 0

Level 1

Level 2

Level 3

Levels 4 and 5 are unnumbered

Stack

4-1 levels under

Processor_registers

Level 0

Level 1

Level 2

3 levels of RAM
- A reported

mode bit - 0

reversed

gene

As best couple

Can
meas \text{ IV}(n) \{ \text{ PS } \rightarrow \text{ PS } \}
\text{ meas } \text{ IV}(n) \{ \text{ PC } \rightarrow \text{ PC } \}
\text{ push PC on the stack}
\text{ CPD } \rightarrow \text{ PC}
\{ \text{ IN , OUT } \}
\text{ interrupt and go on ...}
(head)

stack

read PC from

(stack)

stack

read PS from

ATI ← read PS from
Put on all mode bits
Start the user process with
Open mode bits set to 0
Code, static data (stencil)

Context: I've all IBE

Recall

> fixed meaning

> core part of OS

Kernel — read and code

But why new process
Fly me

Good morning

Are you in line some day

System calls
under the roof

put a sheet of paper

1. move piece to left
move a bag on top

and [maybe a word] and

[illegible]

[illegible]

point ("like used in")
See the use 18F 80

$m_1, 18F \times 80$

an interrupt

IN1 0 $x80 < \text{Processor}^\text{e}

Please 2 save up for a

$R_1$